

**MANCHESTER CODE DELTA DETECTOR****ABSTRACT OF THE DISCLOSURE**

5 A biphasic code detector and method for implementing the same. In accordance with one embodiment, the biphasic code detector includes a receiver input for receiving a biphasic encoded signal. The biphasic encoded signal is a stream of unit bit cells each  
10 having a logic value encoded as a mid-bit transition between a first half-symbol signal component and a second half-symbol signal component. A demodulator demodulates the first and the second half-symbol components of a received unit bit cell. The biphasic code detector further includes a delta detector that generates a difference signal corresponding to the difference between the demodulated values of the first and second half-symbol  
15 components to determine the logic value of the received unit bit cell. In a preferred embodiment, the biphasic code detector incorporates the delta detection function within an optimum receiver that integrates demodulation and detection functionality. For a given received signal energy, the biphasic code detector of the present invention results in an approximate 3dB sensitivity increase in detector gain resulting in a lower probability of  
20 error and lower transmission power requirements.